



2026 RISC-V TAIPEI DAY

SPONSORSHIP PROGRAM

RISC-V Taiwan Alliance

RISC-V IS MOVING BEYOND INNOVATION



INNOVATION PHASE

- Open ISA adoption
- Academic & early adopters
- IP-level discussions

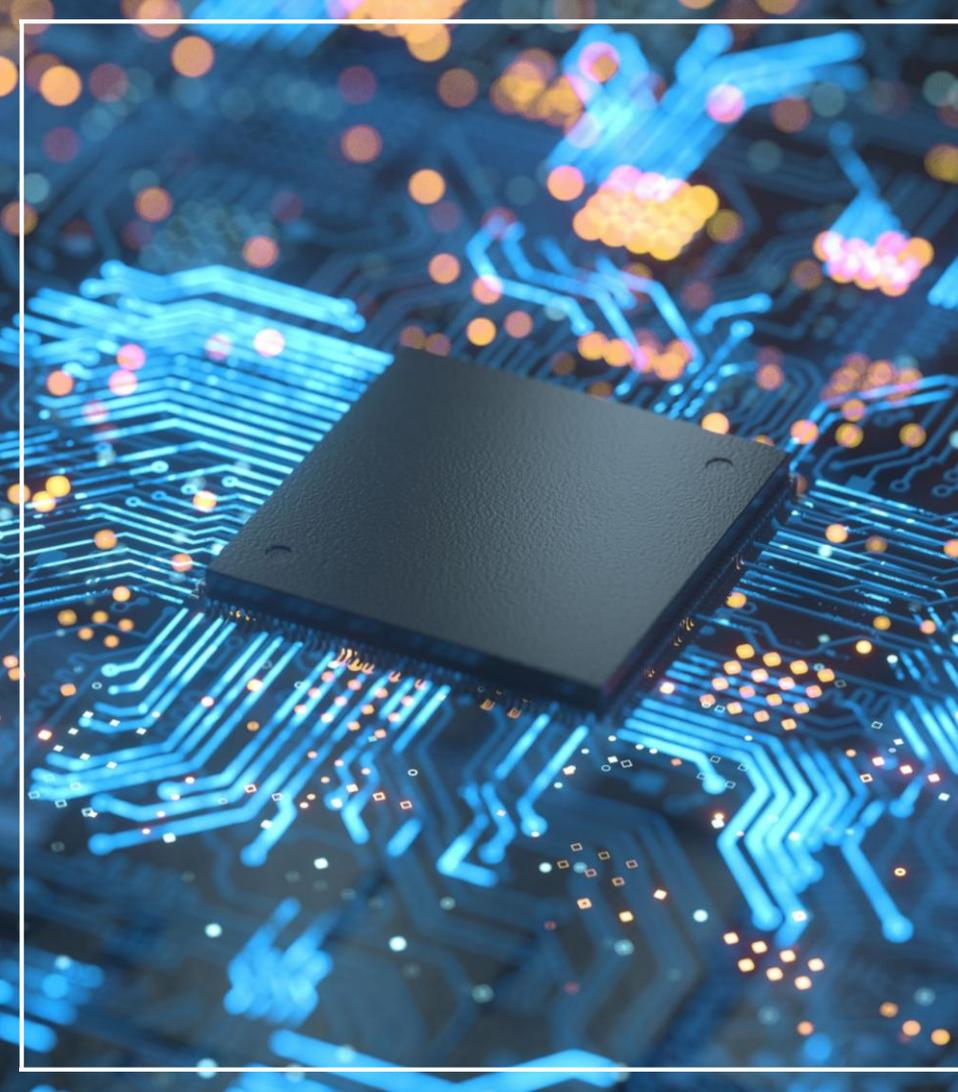
INFLECTION POINT

- Platform readiness
- Ecosystem integration
- Real-world deployment

SCALING PHASE

- Volume production
- Industry standards
- Global platforms





A UNIQUE CONVERGENCE POINT FOR THE GLOBAL RISC-V ECOSYSTEM

- A world-class semiconductor and system supply chain
- Strong integration across IP, design, manufacturing, and systems
- An open and trusted platform for cross-border collaboration
- A natural meeting point for global partners and markets



RISC-V PAVILION

Curated exhibition showcasing the ecosystem



FULL-DAY FORUM

Global insights, industry applications, and thought leadership



MEDIA ENGAGEMENT

Connecting sponsors with local and international media

2026 RISC-V TAIPEI DAY



ABOUT COMPUTEX



COMPUTEX is one of the largest and most influential technology trade shows in the world. It primarily focuses on the computer and information technology industries, showcasing innovations in hardware, software, and cutting-edge technologies.



JUNE 2-5, 2026

NANGANG & TWTC

DUAL VENUE

- **System Integration Solution**
- Gaming
- Consumer Electronic Accessories
- Smart Mobility and Drone Tech
- Advanced Communication & Networking
- Storage & Management Solutions
- TechXperience
- Intelligent Business Solutions
- AI Computing & Tech
- Components & Advanced Power Tech
- Industrial IoT & Embedded Systems
- Robotics
- Semiconductors & Hospitality Suites



2025 SHOW HIGHLIGHTS

1,500 Exhibitors | 4,500 Booths | 85,179 Visitors

Top 10 Regions of Int'l Visitors



Key Exhibitors Registered @ 2026 COMPUTEX



RISC-V PAVILION

M111A, 4F, TAINEX 1

- 20-booth pavilion under a unified RISC-V narrative
- Ecosystem-based zoning and storytelling
- Dedicated media and VIP guided tours
- Alliance endorsement and positioning

SPONSORS X ECOSYSTEM CONTRIBUTORS



CONTENT AND SERVICE AT PAVILION



EQUIPMENT

- Poster*1
- Light*1
- 5A double socket *1 (550W)



INFLECTION POINT

- Platform readiness
- Ecosystem integration
- Real-world deployment



SCALING PHASE

- Volume production
- Industry standards
- Global platforms



EVENT SCHEDULE (SUBJECT TO CHANGE)

JUNE 02

JUNE 03

JUNE 04

JUNE 05



Pavilion
Opening
Ceremony

RISC-V
Taipei Day
Forum

RISC-V Taiwan Pavilion
Showcase @ COMPUTEX 2026



Media
Networking
Luncheon

RISC-V
Taipei Day
Forum



InnoVEX
Night Party

RISC-V
VIP Mixer

RISC-V TAIPEI DAY

FULL-DAY FORUM

2026 RISC-V
Taipei Day

- International keynote speakers
- Application-driven sessions (AI, Edge, Automotive, Security) Panels featuring ecosystem collaboration
- High-quality audience aligned with COMPUTEX professionals



WHAT TO SEE AT RISC-V TAIPEI DAY

**2026 RISC-V
Taipei Day**



KEYNOTE

Insightful tech talks by industry key players from the globe



PRESENTATION

Leading companies in the RISC-V ecosystem to present the latest technology and trends



PANEL DISCUSSION

Inspiring discussion of key players and industry leaders





SPONSOR VALUE

BRAND POSITIONING

01

Recognized as a key player in the RISC-V ecosystem

THOUGHT LEADERSHIP

02

Industry viewpoints amplified through Alliance channels

MEDIA VISIBILITY

03

Access to local and international technology media

ECOSYSTEM CONNECTION

04

Engagement with partners, customers, and developers



RISC-V TAIWAN ECOSYSTEM CONNECTION



經濟部產業發展署
南部晶片設計產業推動基地
Southern Taiwan IC Design Industry Promotion Hub,
Industrial Development Administration, Ministry of Economic Affairs



國立陽明交通大學
NATIONAL YANG MING CHIAO TUNG UNIVERSITY

SPONSOR PROGRAM

		PLATINUM	GOLDEN	SILVER	
		Domestic Company	NTD 420,000 (USD 14,000)	NTD 315,000 (USD 10,500)	NTD 210,000 (USD 7,000)
		Foreign Company	USD 15,400	USD 11,550	USD 7,700
Pavilion	Demo Stand at CPX Pavilion for 4 days	2 units	1 unit	1 unit	
	Pavilion Brochure	✓	✓	✓	
Conference	Track of Presentation	✓	Choose either	×	
	Panel Discussion Participation	✓		×	
	Forum Passes (\$1,000 per ticket)	20	15	10	
	Access to VIP Mixer on June 4 (up to 2 people)	✓	✓	✓	
Promotion	Logo on Event Marketing Materials	✓	✓	✓	
	Promotion Video During Forum and at Pavilion	90 secs	60 secs	30 secs	
	Media Engagement (Pre / Onsite / Post-Event)	✓	✓	✓	
	Media Networking Event at Pavilion	✓	✓	✓	

01

MEDIA BRIEFING

Pre-event media briefings
and trend sharing

02

MEDIA ACCESS

On-site Media Day and
guided tours

MEDIA ENGAGEMENT STRATEGY

03

MEDIA DIALOGUE

Coordinated interviews
and roundtables

04

MEDIA AMPLIFICATION

Post-event feature stories
and content distribution

ATTACHMENT

MEDIA ENGAGEMENT ACROSS PRE / ONSITE / POST-EVENT PHASES

Pre-Event Media Engagement (*April – May 2026*)

- 1) Curated media background briefings to introduce the RISC-V ecosystem, industry trends, and key themes of RISC-V Taipei Day
- 2) Early sponsor visibility in pre-event media context and industry briefings (where relevant)
- 3) Inclusion in media reference materials to support editors' pre-COMPUTEX coverage planning

Onsite Media Engagement (*June 2–5, 2026 @ COMPUTEX*)

- 1) Invitation to **Media Networking Luncheon** on June 3, facilitating informal interactions with invited media
- 2) Opportunities for **onsite media interactions** at the RISC-V Pavilion and forum venue
- 3) Sponsor presence integrated into **media-facing touchpoints**, including background materials and onsite signage

Post-Event Media Engagement (*June – July 2026*)

- 1) Post-event **media recap materials** highlighting key discussions, ecosystem insights, and event outcomes
- 2) Continued **reference visibility** in follow-up media communications and industry summaries
- 3) Support for **media follow-ups** on technical and ecosystem topics discussed during the foru

**2025 RISC-V
Taipei Day**

Pioneering AI with RISC-V

Built on Open Standards, Secure for AI

**RVI Successfully R
RISC-V Open Stan**



Frankwell

Chairman of RISC-V Taiwan
Chairman & CEO of Andes

**POST REVIEW OF
2025 RISC-V TAIPEI DAY**



Click for Recap Video of
2025 RISC-V Taipei Day

**2025 RISC-V
Taipei Day**

1. Event Overview – Forum

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

2025 RISC-V
Taipei Day

Organizer:   TwioTA
Co-Organizer:  RISC-V

May 20-23 **PAVILION**

Booth L0425 4F, TaiNEX 1, Taipei

May 21 **CONFERENCE**

Room 505 5F, TaiNEX 1, Taipei

2 Keynotes | 9 Tech & Trends Presentations | 1 Panel Discussion

15 Sponsors & 4 Partners

PLATINUM    Powerchip  tenstorrent

GOLDEN  Rivos  RISE  DeepComputing  NUCLEI  CHELPIS  semidynamicS

SILVER  南部晶片設計
產業推動基地 | Southern Taiwan
IC Design Industry
Promotion Hub  MEDIATEK
everyday genius  REALTEK  達摩院
DAMO ACADEMY |  XUANTIE  Qualcomm

SPECIAL PARTNER  工業技術研究院
Industrial Technology
Research Institute  neuralscope
國立陽明交通大學人工智慧系統檢測中心  Syntronix  GSA



International Speaker



Frankwell Lin

Chairman,
RISC-V Taiwan Alliance
Chairman and CEO,
Andes Technology



Dr. Alex Wang

Chairman,
TwIoTA



Jian-Heng, Wu

Senior Technical Specialist,
ADI, Ministry of Digital Affairs
(MODA) Taiwan



Lu Dai

Board Chair,
RISC-V International
Senior Director of
Technical Standards,
Qualcomm Technologies



Dr. Charlie Su

President and CTO,
Andes Technology



Luke Yen

Fellow of RISC-V CPU and
Machine Learning
Architectures,
Tenstorrent



Luba Tang

Founder & CTO,
Skymizer



Mark Hayter

Co-Founder and Chief
Strategy Officer,
Rivos Inc.



Yuning Liang

Founder & CEO,
DeepComputing



Nathan Ma

AVP of Marketing and
Strategy,
Nuclei System
Technology



Ming-Yang Chih

Founder & CEO,
Chelpis Quantum
Corporation



Roger Espasa

Founder & CEO,
Semidynamics



Dr. Jefferey Chiu

CEO,
Ansforce Inc.



William Wei

CMO and EVP,
Skymizer



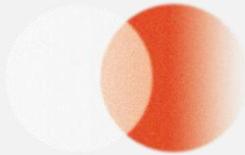
Agenda – Morning Session

Time	Topic	Speaker
09:00 – 10:00	Welcome and Registration	
10:00 – 10:15	Welcome Keynote	Frankwell Lin, Chairman, RISC-V Taiwan Alliance (Chairman & CEO, Andes Technology)
10:15 – 10:20	Welcome Remark	Dr. Alex Wang, Chairman, TWIoTA
10:20 – 10:25	Welcome Remark	Jian-Heng Wu, Senior Technical Specialist, Administration for Digital Industries (ADI), Ministry of Digital Affairs (moda)
10:25 – 10:30	Group Photo	
10:30 – 10:45	Keynote 1	Lu Dai, Board Chair, RISC-V International (Senior Director of Technical Standards, Qualcomm Technologies, Inc.)
10:45 – 11:05	Empowering Innovations in Modern Computing with RISC-V	Dr. Charlie Su, President and CTO, Andes Technology
11:05 – 11:25	Scaling Open Compute: RISC-V, Chiplets, and the Future of AI and Robotics	Luke Yen, Fellow of RISC-V CPU and Machine Learning Architectures, Tenstorrent
11:25 - 11:45	Skymizer Hyper Thought : Hyper-threading Language Processing Units for Large Language Model	Luba Tang, Founder & CTO, Skymizer
11:45 – 11:55	Lucky Draw	
11:55 - 13:30	Lunch Break	



Agenda – Afternoon Session

Time	Topic	Speaker
13:30 – 14:00	Welcome and Registration	
14:00 – 14:15	Unleashing the Potential of RISC-V Through Open Source Collaboration	Ruinland Tsai, Chair of System Library Working Group, RISE
14:15 – 14:30	The Significance of the RVA23 Profile and Platform Specifications in Advancing RISC-V Ecosystem Development	Mark Hayter, Co-Founder and Chief Strategy Officer, Rivos Inc.
14:30 – 14:45	Revolutionizing AI Computing with World's First RISC-V 50 TOPS AI PC	Yuning Liang, Founder & CEO, DeepComputing
14:45 – 15:00	RISC-V accelerating the future of AI & Automotive computing	Nathan Ma, AVP of Marketing and Strategy, Nuclei System Technology
15:00 – 15:15	Autonomy Robotic System Architecture for the Quantum Era — Building AI + PQC Secure Robotics with RISC-V Architecture	Ming-Yang Chih, Founder & CEO, Chelpis Quantum Corporation
15:15 – 15:30	Enabling NPU AI Compute entirely on RISC-V ISA	Roger Espasa, Founder & CEO, Semidynamics
11:25 - 11:45	Panel Discussion	Moderator: Dr. Jefferey Chiu, CEO, Ansforce Inc. Panelists: Frankwell Lin (Chairman, RISC-V Taiwan Alliance), Lu Dai (Board Chair, RISC-V International/Senior Director of Technical Standards, Qualcomm Technologies, Inc.), Dr. Charlie Su (President and CTO, Andes Technology), Luke Yen (Fellow of RISC-V CPU and Machine Learning Architectures, Tenstorrent), William Wei (CMO and EVP, Skymizer)
16:10 – 16:20	Lucky Draw	
16:20 - 16:40	Lunch Break	



On-Site Impressions

Participation: over 210 attendees and 400 registered users



2. Event Overview – Pavilion



About RISC-V Pavilion

18

Sponsors from
IC Industry

12

Exhibitors
at Pavilion

1

Pavilion
Opening
Ceremony

2

Happy Hours
Networking

3

Exhibitor
Technical
Presentation



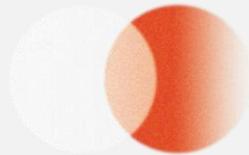


Opening Ceremony

Including welcome remarks, opening ceremony, sponsor group photo and on-site photo/interview opportunities provided for the media

Time	Agenda	Speaker
10:45 – 11:00	Registration	
11:00 – 11:01	Opening Remarks	
11:01 – 11:04	Welcome Speech	Frankwell Lin, Chairman, RISC V Taiwan Alliance
11:04 – 11:07	Remarks	Lu Dai, Board Chair, RISC-V International
11:07– 11:10	Opening Group Photo	Frankwell Lin, Chairman, RISC-V Taiwan Alliance Lu Dai, Board Chair, RISC-V International
11:10 – 11:15	Group Photo: RISC-V Community	Frankwell Lin, Lu Dai, and all sponsor representatives
11:15 – 11:30	Media Tour: Guided Booth Visits	Frankwell Lin, Lu Dai, Media





Exhibitor Demo

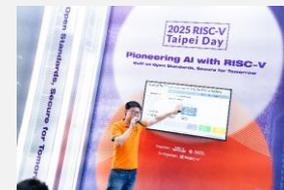
5/22

Time	Agenda	Speaker
11:00 – 11:10	Exhibitor Demo	Skymizer
11:10 – 11:20	Exhibitor Demo	Semidynamics
11:20 – 11:30	Exhibitor Demo	DeepComputing
11:30 – 11:40	Exhibitor Demo	NYCU
11:40– 11:50	Exhibitor Demo	Nuclei System
11:50 – 12:00	Exhibitor Demo	STICH
12:00 – 12:10	Exhibitor Demo	Andes
14:30 – 14:40	Exhibitor Demo	Andes
14:40 – 14:50	Exhibitor Demo	Nuclei System
14:50 – 15:00	Exhibitor Demo	DeepComputing
15:00 – 15:10	Exhibitor Demo	Skymizer
15:10 – 15:20	Exhibitor Demo	Semidynamics
15:20 – 15:30	Exhibitor Demo	NYCU

5/23

Time	Agenda	Speaker
11:00 – 11:10	Exhibitor & Partner Demo	Skymizer / 安國國際科技
11:10 – 11:20	Exhibitor & Partner Demo	Andes

Exhibitor Demo: 10 minutes demo by the exhibitors
Exhibitor & Partner Demo: 30 minutes demo by the exhibitors and their partners





Happy Hour

Date: 5/20 & 5/22

Time: 14:00 – 14:30

Event Detail:

Activities held at different times over the exhibition period purposed to gather the crowd, with beer, soft drinks, and tea provided



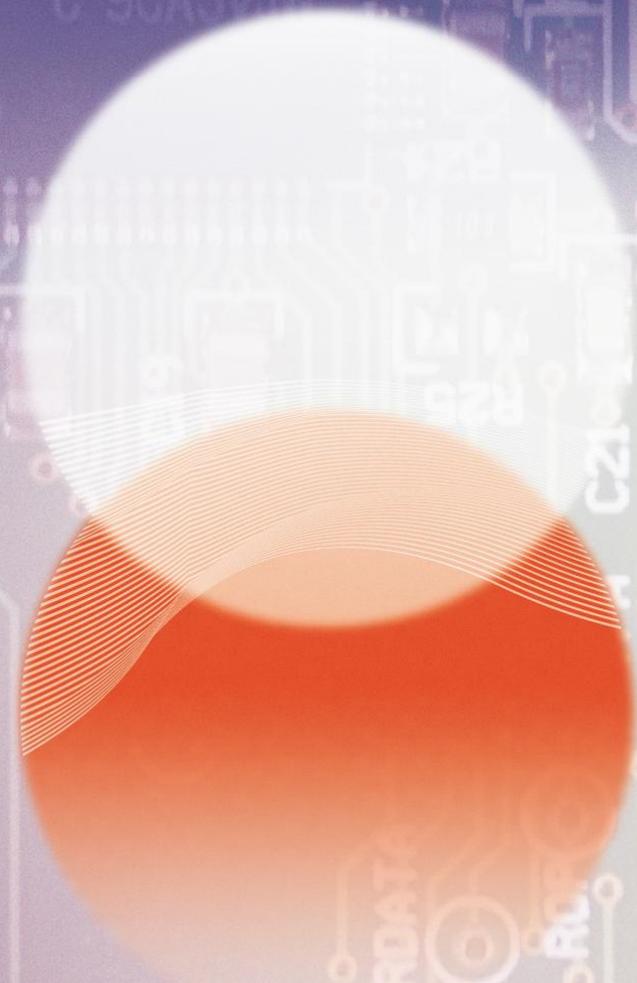


On-Site Impressions



2025 RISC-V
Taipei Day

3. Event Marketing





Landing Page

- Chinese Ver.
<https://www.twiota.org/RISC-V/Pioneering-AI-with-RISC-V/2025/index.html>
- English Ver.
https://www.twiota.org/RISC-V/Pioneering-AI-with-RISC-V/2025/index_en.html

關於活動 RISC-V Pavilion RISC-V Taipei Day 嘉賓介紹 贊助夥伴 2025 EN

2025 RISC-V Taipei Day

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

May 20-23 PAVILION Booth L0425-4F, TaINEX 1, Taipei | May 21 CONFERENCE Room 505-5F, TaINEX 1, Taipei

Organizer: RISC-V, Twiota, Co-Organizer: FRISCV

Pioneering AI with RISC-V - Built on Open Standards, Secured for Tomorrow

RISC-V 架構經過十年發展，在 AI SoC 領域展現強勁成長動能，並在晶片安全方面具有潛力。今年 RISC-V Taipei Day 首度於 COMPUTEX 台北國際電腦展舉辦，規模空前，包含四天主題展示與 5 月 21 日的全日論壇，聚焦 AI 計算與安全應用趨勢。

隨著 AI 計算需求爆發，RISC-V 架構將進化 AI 加速器（NPU、TPU）整合等提升效能，非 RISC-V SoC 可支援 AI 推理、優化訓練與推理，並強化邊緣 AI 推理，減少對雲端依賴，提高隱私與安全性。此外，RISC-V 開放架構允許開發者檢視晶片設計，降低安全漏洞風險，並可依據需求充實安全模組，增強防護能力。

RISC-V 具備彈性及靈活定制化優勢，推動 AI 在雲端、邊緣 AI、自駕車、智慧醫療、AIoT 等領域的發展，並促進技術民主化，加碼全球創新。台灣 RISC-V 聯盟也將持續推動國際 AI 硬體與軟體生態，帶動 AI 晶片設計發展，共同見證 RISC-V 開創 AI 與安全應用新紀元。

RISC-V Pavilion @ COMPUTEX

Time: May 20-23
Location: L0425, 4F, Nangang Exhibition Hall 1

RISC-V Conference

Time: 09:00-17:00, May 21
Location: Room 505, 5F, Nangang Exhibition Hall 1

2025 RISC-V Taipei Day



EDM

2025 RISC-V Taipei Day

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

May 20-21 FAVELON
Room: 505A, Taipei, Taiwan

May 21 CONFERENCE
Room: 505, Taipei, Taiwan

Organizer:

4 週前
早鳥預購

RISC-V 架構在 AI SoC 領域持續展現強勁成長動能，並在晶片安全方面具有不可忽視的潛力。今年 RISC-V Taipei Day 首度於 COMPUTEX 台北國際電腦展舉辦，匯集全球「含苞待放」主題展中與今年 5 月 21 日的全球領導「專業 AI 計算與安全應用趨勢」。

邀請來自台灣 RISC-V 聯盟 持續推動臺灣 AI 硬體與軟體生態，推動 AI 晶片設計發展，共同開拓 RISC-V 最新 AI 開發安全運算新紀元！

Keynote Speaker

Frankwell Lin
RISC-V System Architect
Chairman
Amda Technology
Chairman and CEO

Keynote Speaker

Lu Dai
RISC-V Development
Global Chair
Qualcomm
Senior Director Technical Network

Panel Moderator

Dr. Jefferey Chiu
Rafaela Inc.
CEO

Technical Sharing



完整生態系統對真展示

2 場 國際趨勢 / 10 場 專業技術 / 13 家 展示論壇

Organizer: Co-Organizer:

2025 RISC-V Taipei Day

COMPUTEX 首場 AI 晶片開放標準論壇

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

May 21 10:00-16:30 南港展覽館一館5樓 505會議室

！ 座位有限 立即報名 **！ 抽中 iPad**

開放指令集 (RISC-V) 正在重塑 AI 晶片設計的未來！今年的 RISC-V Taipei Day 將於 COMPUTEX 展中登場，邀請來自全球的技术領袖，共同探討 RISC-V 如何加速 AI 與高性能運算的創新應用，屆時邀請您一同與技術先鋒面對面，搶先掌握 AI 晶片運算趨勢！

10 場國內外技術專家重磅登場

Frankwell Lin Chairman, RVTA Chairman and CEO, Amda	Lu Dai RISC-V Development Global Chair Qualcomm Senior Director Technical Network	Dr. Jefferey Chiu Rafaela Inc. CEO
Ying Liang Founder and CEO, DeepComputing	Jianying Peng CEO, Mobile System	Ming-Yang Chiu CEO, Chiplet Quantum
Roger Espasa Founder & CEO, Seraphimics	William Wei CMO & EVP, Regeneron	

Organizer: Co-Organizer:

2025 RISC-V Taipei Day

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

COMPUTEX 首場 AI 晶片開放標準論壇

May 21 10:00-16:30 南港展覽館一館5樓 505會議室

！ 座位有限 立即報名 **！ 抽中 iPad**

國際講師陣容亮相！

Frankwell Lin Chairman, RVTA Chairman and CEO, Amda	Lu Dai RISC-V Development Global Chair Qualcomm Senior Director Technical Network	Dr. Jefferey Chiu Rafaela Inc. CEO
Ying Liang Founder and CEO, DeepComputing	Jianying Peng CEO, Mobile System	Ming-Yang Chiu CEO, Chiplet Quantum
Roger Espasa Founder & CEO, Seraphimics	William Wei CMO & EVP, Regeneron	

Organizer: Co-Organizer:

COMPUTEX TAIPEI

2025 RISC-V Taipei Day

COMPUTEX 首場 AI 晶片開放標準論壇

Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

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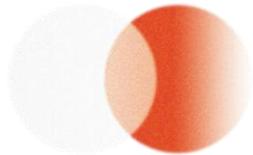
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10 場國內外技術專家重磅登場

Frankwell Lin Chairman, RVTA Chairman and CEO, Amda	Lu Dai RISC-V Development Global Chair Qualcomm Senior Director Technical Network	Dr. Jefferey Chiu Rafaela Inc. CEO
Ying Liang Founder and CEO, DeepComputing	Jianying Peng CEO, Mobile System	Ming-Yang Chiu CEO, Chiplet Quantum
Roger Espasa Founder & CEO, Seraphimics	William Wei CMO & EVP, Regeneron	

Organizer: Co-Organizer:



Brochure

<https://www.twiota.org/riscvpavilion.pdf>

2025 RISC-V Taipei Day

Pioneering AI with RISC-V
Built on Open Standards, Secure for Tomorrow

May 20-23 PAVILION
Booth L0425 4F, TaiNEX 1, Taipei

May 21 CONFERENCE
Room 505 5F, TaiNEX 1, Taipei

Organizer: Co-Organizer:

2025 RISC-V Taipei Day

Pioneering AI with RISC-V
Built on Open Standards, Secure for Tomorrow

Register Now!

May 21 10:00-18:30 Room 505, 5F, TaiNEX 1

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- RISC-V
- AI/ML Project
- Deep Learning
- Kernel
- Security
- Southwest Taiwan Design Industry Promotion Hub, Industrial Development Administration, Ministry of Economic Affairs
- Inteltek
- Alibaba DAMO
- National Yang Ming Chiao Tung University AI System Benchmarking and Tuning Lab

Sponsor & Partner

2025 RISC-V Taipei Day RISC-V Taiwan Pavilion

ANDES Andes Technology

As a Founding Premier member of RISC-V International and a leader in commercial CPU IP, Andes Technology is driving the global adoption of RISC-V. With a comprehensive portfolio ranging from ultra-efficient 32-bit CPUs to high-performance 64-bit Out-of-Order multiprocessor clusters, Andes has enabled the shipment of over 16 billion SoCs powering innovation around the world.

Product Name:
AndeCore™ Summary

Product Summary:
AndeCore™ is made up of a series of high performance 32-bit/ISA-bit CPU core families specially designed to target different market segments of today's emerging applications. The AndeCore™ processors are based on the AndeStar™ V0 instruction set architecture (ISA) and are compliant with RISC-V open standard. According to the requirements of different targeted applications, the AndeCore™ processor supports the following CPU series:

1. Embedded Control: This is the most basic single core CPU running RTOS as controller.
2. Compute Acceleration: This for CPU with SIMD engines for heavy parallel compute task, including cores with 32bit or 64bit wide DSP extension or the high performance vector core with up to 2048 bit wide vector.
3. Linux AP: This core supports latest RISC profile for open source SW compatibility and support multi-core and MMIO for running Linux OS or Android.
4. ISA: This core supports ISO26262 for automotive safety, with either ASEL-B or ASEL-D support.

Product Name:
AndeStar™ AndeCore™ E370

Product Summary:
AndeStar™ AndeCore™ E370 is a performance-efficient deep learning accelerator for CNN/RNN models, INT8/INT16 datatype, and more configurations of the MAC number, local memory size and bus width. It's designed to enable high-performance and cost sensitive AI solutions for edge/endpoint inference and provides flexible computing power up to 2 TOPS (at 1 GHz). It supports popular deep-learning frameworks, such as TensorFlow Lite, PyTorch, and ONNX.

ANDES

Andy Hung Kuang Ho <https://www.andes.tech/tw/>
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Pioneering AI with RISC-V論壇21日登場 聚焦AI運算與安全應用趨勢

本文共512字



2025/05/08 16:29:44

聯合報 記者簡永祥／台北即時報導

為讓產業人士更加了解 RISC-V 架構與特性，台 RISC-V 聯盟將於21日南港展覽館舉行 RISC-V Taipei Day 論壇 - Pioneering AI with RISC-V, Secure for Tomorrow。

這場論壇將由數位發展部數位產業署署長林俊秀、台灣物聯網產業技術協會理事長王其國到場致詞，並邀請台灣 RISC-V 聯盟會長暨晶心科技董事長暨執行長林志明、RISC-V Internal 董事會主席 Lu Dai 等人聚焦AI運算與安全應用趨勢發表專題演講。



《COMPUTEX》RISC-V主題館聚焦AI/安全應用

2025/5/9 科技脈動

讚 0 分享

MoneyDJ新聞 2025-05-09 10:45:24 記者 羅毓嘉 報導隨著雲端與邊緣AI運算需求持續上升，各類AI SoC晶片需求快速增加。

採用開放標準的RISC-V架構，歷經十年發展後，在AI運算領域展現成長潛力，特別是在晶片安全與彈性客製化方面具備優勢。今年COMPUTEX將首度設立「RISC-V主題館」，集結18家國內外業者展示RISC-V架構應用於高性能、低功耗、多用途AI晶片與解決方案。

今年的COMPUTEX展會期間，將於5月21日舉辦「Pioneering AI with RISC-V, Secure for Tomorrow」全日論壇，聚焦AI運算與安全應用趨勢。

台灣RISC-V聯盟說明，AI運算需求快速擴張，推動RISC-V架構與AI加速器（如NPU、TPU）整合。RISC-V SoC可支援AI訓練與推理效能優化，強化邊緣運算能力，降低對雲端的依賴，有助提升運算效率與資料隱私。此外，由於RISC-V採開放架構，開發者能檢視設計細節、降低安全漏洞風險，並



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海內外18家廠商籌組RISC-V主題館 以開放標準加速AI運算全面普及

2025 RISC-V Taipei Day首度於COMPUTEX期間同步登場

2025.05.09 / 16:11 / 工商時報 文 / 陳宗慶



COMPUTEX TAIPEI

2025 RISC-V Taipei Day

COMPUTEX 首場 AI晶片開放標準論壇

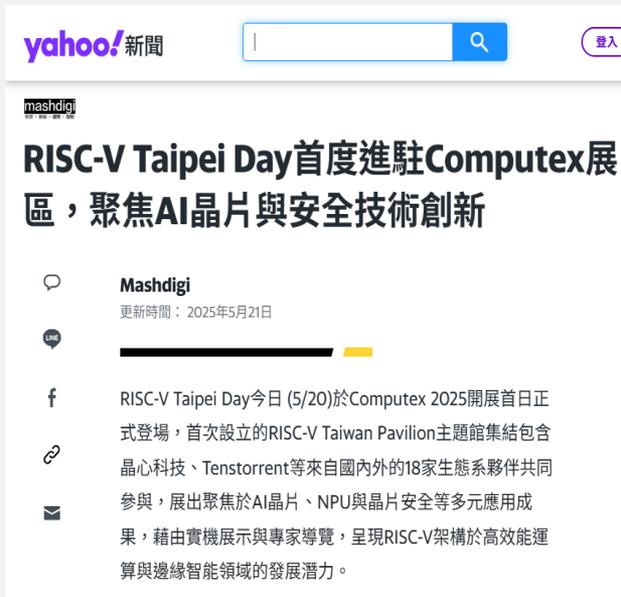
Pioneering AI with RISC-V

Built on Open Standards, Secure for Tomorrow

May 21 10:00-16:30 南港展覽館一館5樓 505會議室

座位有限 立即報名

參與即有機會 抽中 iPad



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登入

mashdigi

RISC-V Taipei Day首度進駐Computex展區，聚焦AI晶片與安全技術創新

Mashdigi

更新時間：2025年5月21日

RISC-V Taipei Day今日 (5/20)於Computex 2025開展首日正式登場，首次設立的RISC-V Taiwan Pavilion主題館集結包含晶心科技、Tenstorrent等來自國內外的18家生態系夥伴共同參與，展出聚焦於AI晶片、NPU與晶片安全等多元應用成果，藉由實機展示與專家導覽，呈現RISC-V架構於高效能運算與邊緣智能領域的發展潛力。



聯合新聞網

即時 以伊衝突 要聞 娛樂 運動 全球 社會 地方 產經 股市 房市 生活 寵物

快訊 颶風「聖帕」今生成！氣象粉專示警：下周1區域再生成颶風機... 15:49

udn / 產經 / 科技產業

Pioneering AI with RISC-V論壇21日登場 聚焦AI運算與安全應用趨勢

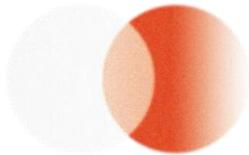
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RISC-V Trend and AI PANEL DISCUSSION

Moderator: Frank Chen

Panelists: Mr. ... Mr. ... Mr. ... Mr. ... Mr. ...



Video Clip Review

COMPUTEX Official Channel on YouTube:

https://www.youtube.com/playlist?list=PLGOkpaKwmygChAAPjP_HQglBmSrXhXXUs

2025 RISC-V Taipei Day @ COMPUTEX
由COMPUTEX建立
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全部播放



1 2025 RISC-V Taipei Day – Pioneering AI with RISC-V

COMPUTEX · 觀看次數：93次 · 3 週前



2 【2025 RISC-V Taipei Day】 RISC-V Taiwan Alliance: Frankwell Lin

COMPUTEX · 觀看次數：33次 · 2 週前



3 【2025 RISC-V Taipei Day】 RISC-V International: Lu Dai

COMPUTEX · 觀看次數：34次 · 2 週前



4 【2025 RISC-V Taipei Day】 Andes Technology: Dr. Charlie Su

COMPUTEX · 觀看次數：42次 · 2 週前



5 【2025 RISC-V Taipei Day】 Tenstorrent: Luke Yen

COMPUTEX · 觀看次數：149次 · 2 週前



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2026 RISC-V TAIPEI DAY



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