

**RISC-V**

TAIWAN

台灣RISC-V聯盟  
RISC-V Taiwan Alliance  
(RVTA)

November, 2023

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# 01. OVERVIEW

# Background and Milestone

**Mar 7<sup>th</sup> , 2019**

**RVTA initiated and established by Taiwan IoT Technology and Industry Association**

**May 29<sup>th</sup>, 2019**

**RVTA hosted the RISC-V forum at COMPUTEX TAIPEI**

**Jul 30<sup>th</sup> & Sep 17<sup>th</sup>, 2019**

**RVTA co-hosted cross-strait standards forum in RISC-V section**

**Sep 25<sup>th</sup>, 2020**

**Named the RISC-V event in Taipei as RISC-V Taipei Day for the first time : RISC-V x Edge AI**

**Oct 12<sup>th</sup>, 2021**

**RISC-V Taipei Day : The Next Golden Decade of Future Computing Architecture**

**Sept 15<sup>th</sup>, 2022**

**RISC-V Taipei Day : Drive Into the Future with RISC-V Inside**

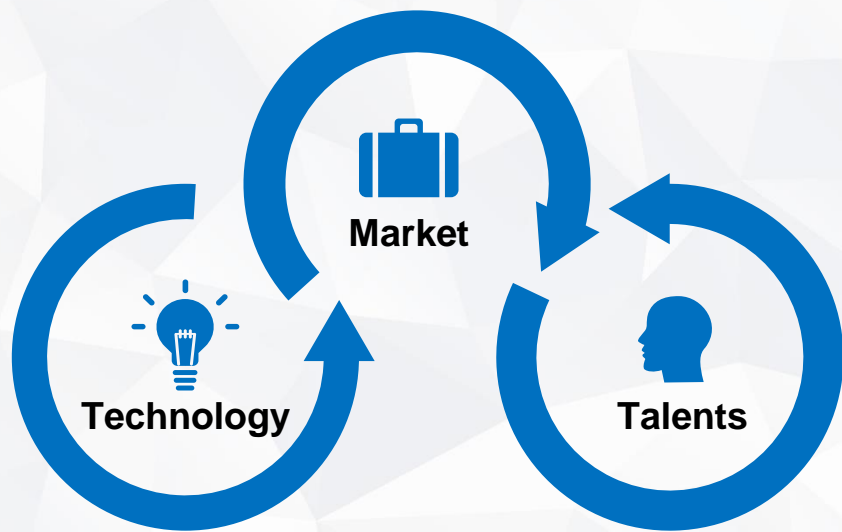
**Oct 12<sup>th</sup>, 2023**

**RISC-V Taipei Day: Unlocking the Possibilities of RISC-V in the AI Era**



# Vision and Mision

***RISC-V Taiwan Alliance (RVTA) is committed to coordinating industry, academic, and research institute to work together and introduce RISC-V open architecture to Taiwan.***



## **Technology**

Promote RISC-V technology and accelerate the development of AIoT and edge computing industry in Taiwan.



## **Market**

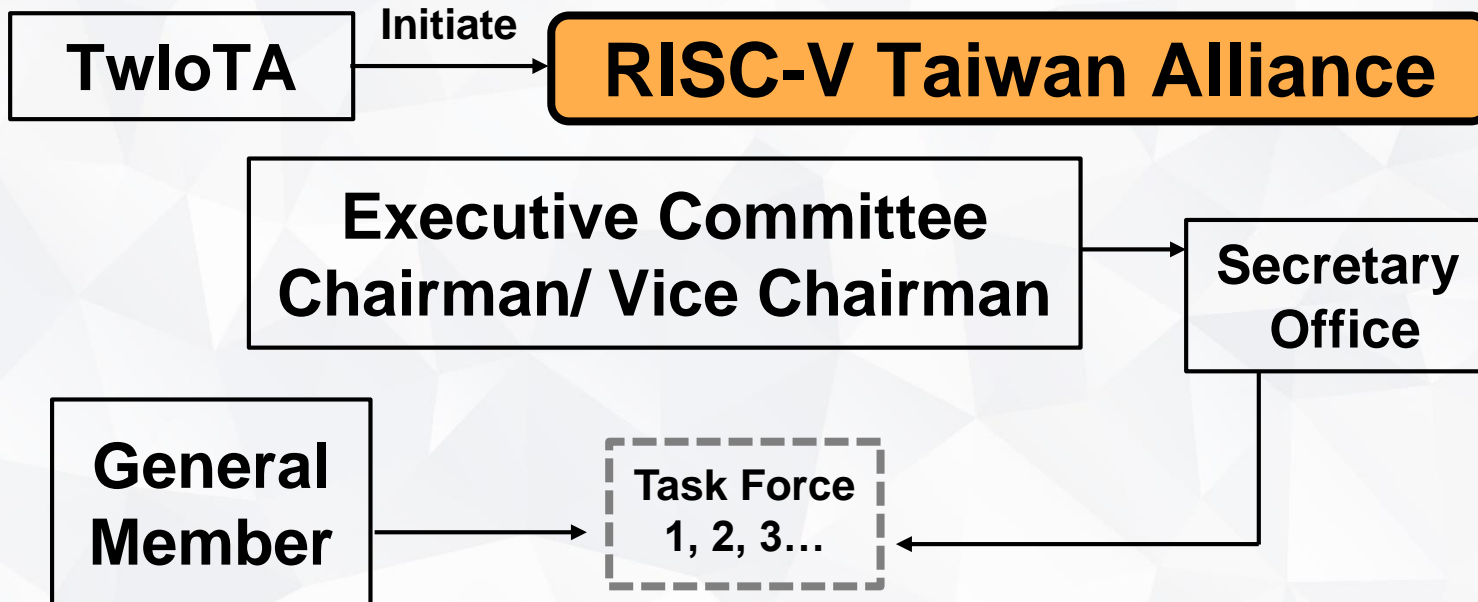
Support Taiwan's semiconductor business to enter the core customization of IC design and expedite the RISC-V open architecture into commercial market.



## **Talents**

Cultivate and connect talent resources from RISC-V ecosystems worldwide, making Taiwan's ICT industry a key player in the global supply chain and core AIoT solution provider.

# Organization Structure



- Executive committee meetings and task force meetings
- Opinion exchanges and discussions

## Chairman



**林志明 Frankwell, Lin**  
Chairman and CEO  
Andes Technology

## Vice Chairman



**黃振昇**  
Jen-Sheng, Hwang  
President, Syntronix



**陳添福**  
Tien-Fu, Chen  
Associate Dean / Professor, NYCU

## Executive Committee



**黃英哲**  
Ing-Jer, Huang  
Professor  
NSYSU



**李政崑**  
Jenq-Kuen, Lee  
Professor  
NTHU



**邱瀝毅**  
Lih-Yih, Chou  
Professor  
NCKU



**楊家驤**  
Chia-Hsiang, Yang  
Professor  
NTU



**劉峻誠**  
Albert Liu  
Chairman/Founder  
Kneron Taiwan



**盧俊銘**  
Jun-Ming, Lu  
Division Director  
ITRI



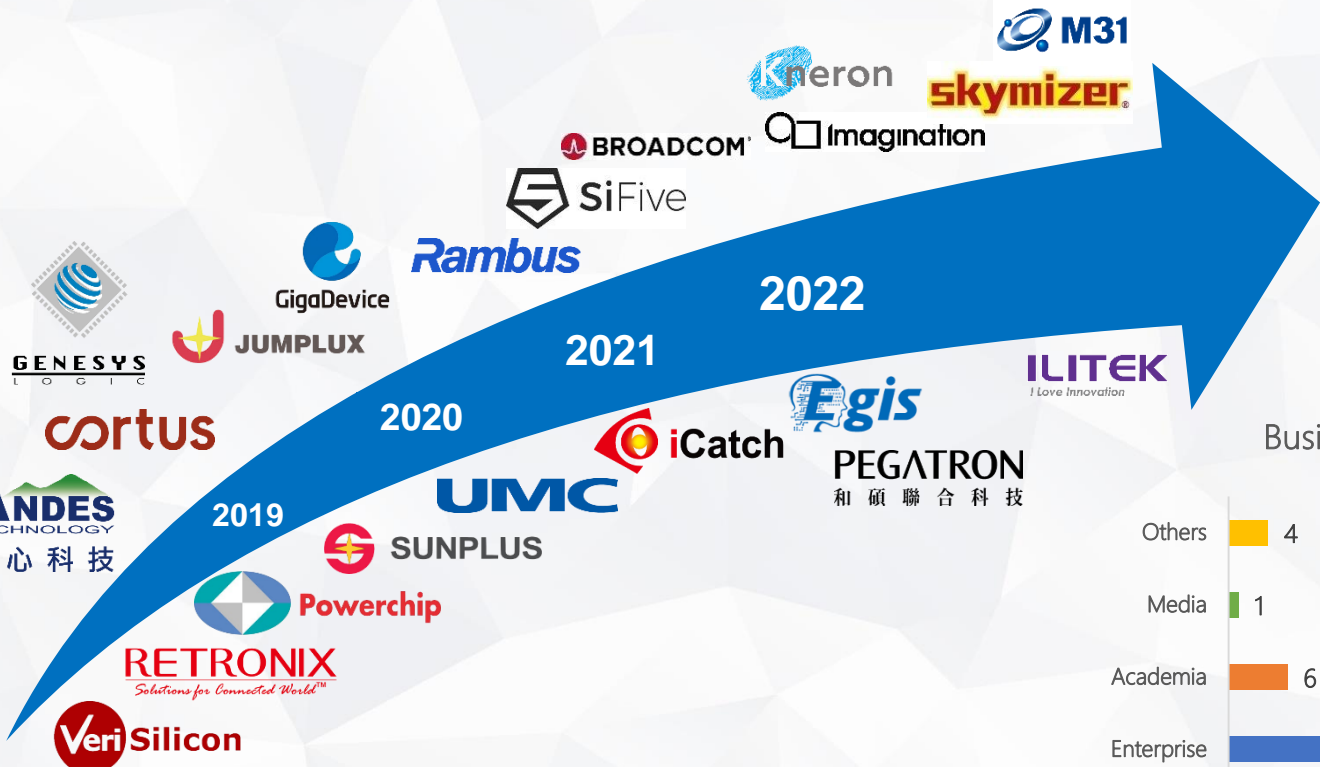
**劉廣治**  
K.C. Liu  
Executive VP  
Skyμιizer

## **02. OUR MEMBERS**





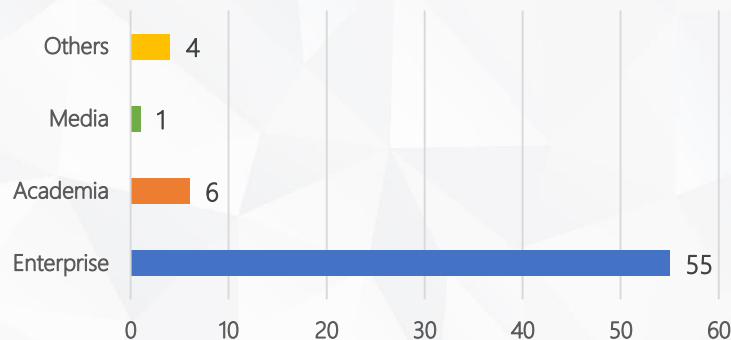
# RVTA Members



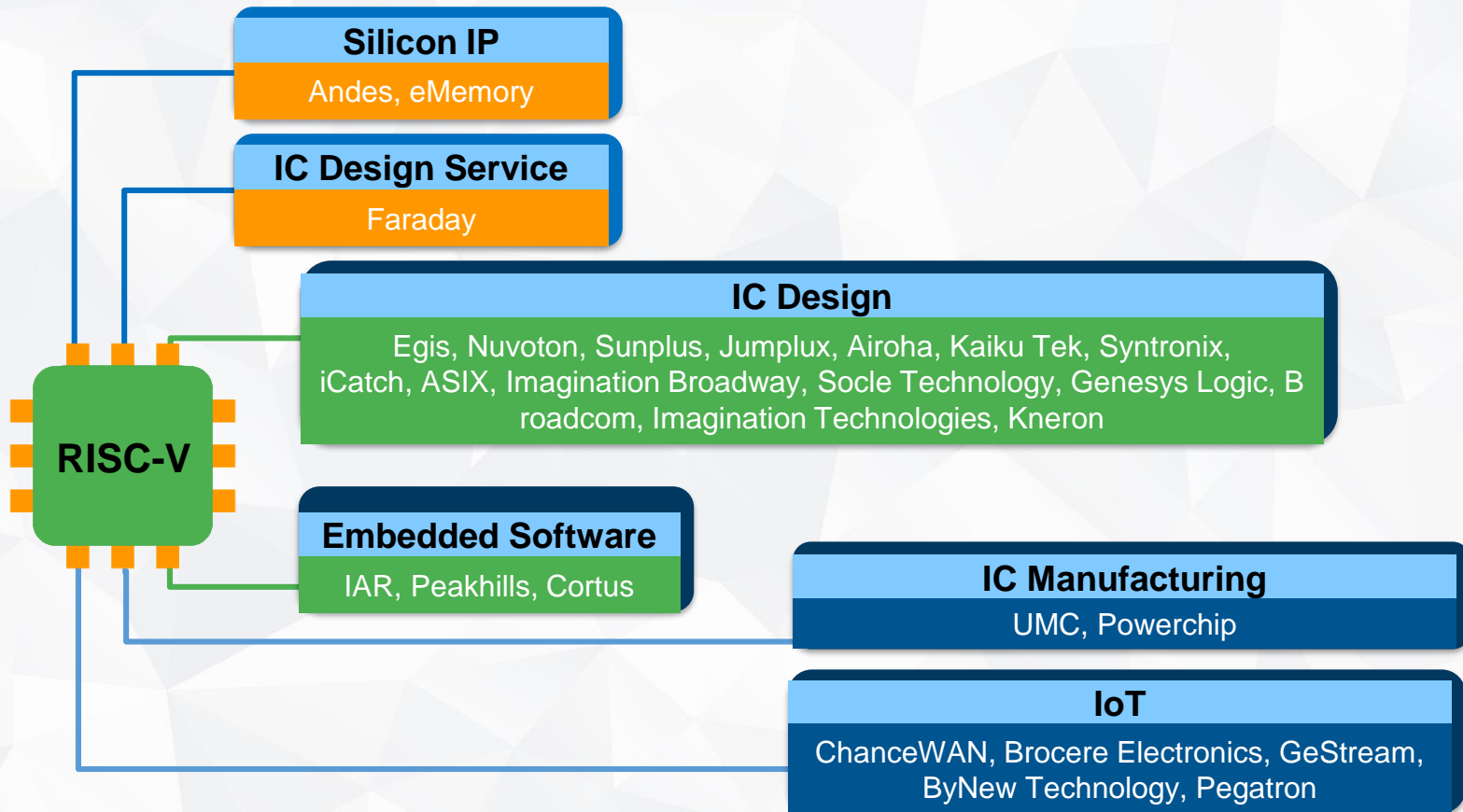
Overall Membership	
Business	Individual
66	357

As of November, 2023

Business Member Breakdown



# RISC-V Ecosystem of RVTA Members



# **O3. MARKETING EFFORTS**

Content Management (as of January 2023)		
Industry News	Buzz & Trends	Member Updates
116	40	77

**產業新聞**

[產業新聞] Semidynamics launches first fully-coherent RISC-V Tensor unit to supercharge AI applications  
發佈日期: 2023/10/27

[產業新聞] 晶心科技與聯發科攜手打造全新生態的AI專用處理器 搭載AndeCore™ D25P RISC-V 處理器核心  
發佈日期: 2023/10/24

[產業新聞] 晶心科技與聯發科攜手打造全新生態的AI專用處理器 搭載AndeCore™ RISC-V 多核心向量處理器 AIx50MPV  
發佈日期: 2023/10/24

[產業新聞] 聯發科所不在的AI論壇登場 2023 RISC-V Taipei Day成功舉辦  
發佈日期: 2023/10/19

[產業新聞] SiFive宣布加速成式AI和ML應用 提供異構化解決方案  
發佈日期: 2023/10/19

[產業新聞] Tenstorrent Selects Samsung Foundry to Manufacture Next-Generation AI Chiplet  
發佈日期: 2023/10/12

**趨勢情報**

[趨勢情報] Qualcomm to Bring RISC-V Based Wearable Platform to Wear OS by Google  
發佈日期: 2023/10/18

[趨勢情報] World's First RISC-V Laptop Roma Officially Delivered: 8GB RAM and Pre-installed with Domestic OS  
發佈日期: 2023/08/21

[趨勢情報] Esperanto Merging HPC and ML in Upcoming RISC-V Processor  
發佈日期: 2023/08/21

[趨勢情報] Chinese Researchers Used AI to Design RISC-V CPU in Under 5 Hours  
發佈日期: 2023/10/14

[趨勢情報] The Linux Foundation Europe launches RSE, the RISC-V Software Ecosystem project  
發佈日期: 2023/06/22

[趨勢情報] RISC-V chip to drive next generation app store of wearables  
發佈日期: 2023/01/06

**會員訊息**

[會員訊息] RISC-V Summit China 2023 kicks off in Beijing  
發佈日期: 2023/08/04

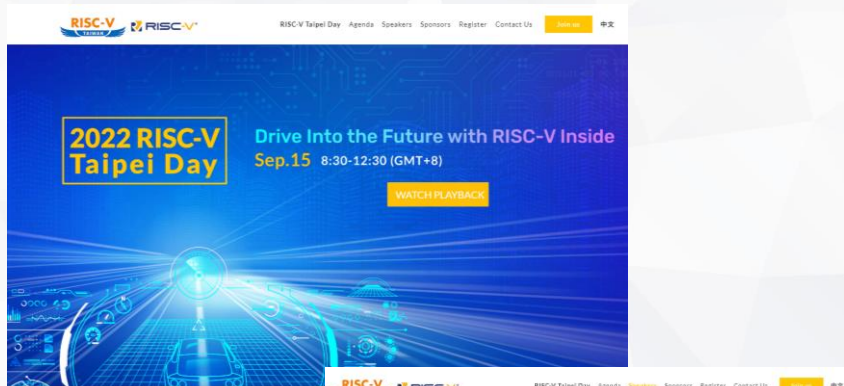
[會員訊息] 10.12 Thursday 晶心科技與聯發科攜手打造全新生態的AI專用處理器 2023 RISC-V Taipei Day 同場舉辦  
發佈日期: 2023/08/22

[會員訊息] Terapines ZCC Toolchain Fully Supports Andes RISC-V Processors  
發佈日期: 2023/08/01

[會員訊息] RISC-V Summit Europe 2023: Highlights from Barcelona  
發佈日期: 2023/07/07

[會員訊息] The Call for Papers for RISC-V Summit China 2023 is Open  
發佈日期: 2023/06/30

[會員訊息] "AndeSentry™" Collaborative Framework Enables Comprehensive RISC-V Security Solutions  
發佈日期: 2023/05/31



2022 RISC-V Taipei Day  
Drive Into the Future with RISC-V Inside  
Sep.15 8:30-12:30 (GMT+8)  
WATCH PLAYBACK

## 2022 RISC-V Taipei Day

2022 saw the rise of RISC-V design adoption as companies ranging from Technology to Taiwan based RISC-V IP leaders, and more adopt the design end automotive solutions. This trend is expected to grow even further as global MCU sales to reach a record high of USD 21.5 Billion in 2022 with growth expected to outpace most of the other end-use categories.

This year, the RISC-V Taipei Day will target on Net Zero Emissions with fo

## SPEAKERS



Dr. Alex Wang  
President, Powerchip Technology  
(Chairman of RISC-V Taiwan Alliance)



Jo-Ann Su  
Senior Director - Business Development, Strategic  
Partnership, Sales and Marketing  
SEMTE Taiwan



Catalina Redmond  
CEO  
RISC-V International



Frankwell Lin  
Chairman and CEO, Andes Technology  
Board of Director, RISC-V International



Gary Martz  
Sr. Director,  
Intel Foundry Services, RISC-V Enablement, Intel



Charlie Su  
President and CTO  
Andes Technology



Tommy Lin  
Senior Manager, Product Marketing  
SPiRE



James Syy  
Senior Principal and Application Engineer  
Imagination Technologies



Vishakh Rayapeta  
Senior Technical Staff Engineer,  
PRGA Applications,



10.12 Thursday  
台北國際會議中心 TICC 201會議室  
Taipei International Convention Center (TICC) Room 201

Morning Session  
09:00-12:00  
IEEE CASIF Taipei

Afternoon Session  
13:00-17:00  
2023 RISC-V Taipei Day

## IC Tech in the AI Era

The development of AI computing has reached a critical inflection point. Large-scale require huge computation for AI model training and inference. In the AI era, IC continuously shrink semiconductor process to meet the targets on performance, performance, bandwidth, and energy efficiency. Complex IC designs also increase the

At the same time, AI also enables new opportunities: AI can create new function efficiency of the IC industry. Meanwhile, open source processor architectures can p computing architecture. These together have brought revolutionary new opportunit

On October 12th, join us for the most influential IC industry event "IC Tech in the AI Era" explore these topics with us. In the morning session, "CASIF Taipei 2022" hosted by IEEE Through technical speeches from TSMC and MediaTek, as well as panel discuss institutes, we will discuss the future directions and opportunities of IC design in this Day". Hosted by RISC-V Taiwan Alliance (RVTAA), the forum will explore how the RISC systems and AI computing architectures in the AI era. These valuable events are fr state-of-the-art IC technology to attend. Lunch will be provided between the two eve

We especially recommend you to join the "YST Forum" held by TIARA (Taiwan Semitec (Friday) which will be held on the 2nd floor at Taipei World Trade Center (TWTC) our talents understand their career path on the IC and semiconductor fields.



Dr. Alex Wang  
Chairman,  
Taiwan IoT Technology and Industry Association,  
TICITA



Frankwell Lin  
Chairman, RISC-V Taiwan Alliance  
Chairman and CEO, Andes Technology



Li Dai  
Board Chair  
RISC-V International  
Senior Director, Technical Standards, Qualcomm



Catalina Redmond  
CEO  
RISC-V International



Dr. Philipp Tomisch  
Founder and Chief Technologist, VULIX  
(Chair of the Applications & Tools Committee,  
RISC-V International)



Wei-Han Lien  
Chief CPU Architect and Senior Fellow  
Architecture  
Tensortron



Dr. Charlie Su  
President and CTO, Andes Technology;  
Member of the Technical Steering Committee of  
RISC-V International  
Member of the Governing Board of RISE project  
(RISC-V Software Ecosystem)



William Wei  
CEO & SVP  
Slymizer



Prof. Tien-Fu Chen  
Associate Chair / Professor  
Department of Computer Science,  
National Yang Ming Chiao Tung University



Jim Liu  
Chairman of the Board  
Slymizer



Tsao Lin  
Vice President  
Sales Marketing, Synopsys Taiwan



Michael Wang  
Associate Vice President  
Corporate Marketing, UMC

## 2022 RISC-V Taipei Day



2022 saw the rise of RISC-V design adoption as companies ranging from Intel to Renesas, Andes Technology (a Taiwan based RISC-V IP vendor), and more adopt the design for their next-gen high and automotive solutions. This trend is expected to grow even further as IC insights forecasted the global MCU sales to reach a record high of US\$21.5 Billion in 2022 with automotive MCU segment growth expected to outpace most of the other end-use categories. This year, the RISC-V Taipei Day will target on Intel Zero Emulators with focus on electric and autonomous vehicles. The forum will invite international and domestic experts to share their insights on RISC-V and how the flexibility and scalability of RISC-V can help in the diverse development of automotive systems and solutions. Join us as we welcome the possibilities of high-end RISC-V automotive solutions.

Exhibitor List


## 2023 RISC-V Taipei Day



RISC-V has seen significant developments in 2023. A number of major manufacturers, including QUALCOMM, Xilinx, Broadcom, and Renesas have announced a joint venture for RISC-V architecture in automotive electronics. Intel has also unveiled the first RISC-V based chip "JUPITER" in May this year which is expected to be launched in 2025. Meanwhile, Google also announced the launch of support RISC-V architecture on the Linux. AMD also expressed their interest in exploring RISC-V with the establishment of RISE (RISC-V Software Ecosystem) which includes global ICT and semiconductor giants such as Samsung Electronics, NVIDIA, Intel, Google and Qualcomm.

2023 RISC-V Taipei Day, hosted by RISC-V Taiwan Alliance (RTA), invited speakers representing local and international industry experts to share their insights on the trends and developments of RISC-V over the years. Focus on RISC-V's role in AI Computing and explore the potentials of RISC-V open source architecture in the AI era.

Exhibitor List


1 2 Next >




- 2022 RISC-V Taipei Day: Drive Into the Future with RISC-V Inside  
COMPUTEX · 觀看次數: 7447 · 直播時間: 4 分鐘
- How RISC-V Helps Your Competitors by Andes Technology  
COMPUTEX · 觀看次數: 3407 · 1 分鐘
- RISC-V is a Mega Trend by Andes Technology  
COMPUTEX · 觀看次數: 3181 · 1 分鐘
- 5Five RISC-V Processors for Automotive and Safety Critical Applications by SiFive  
COMPUTEX · 觀看次數: 3487 · 1 分鐘
- IMG RTKM-2200: Imagination's First Licensable RISC-V Catapult CPU by Imagination Technologies  
COMPUTEX · 觀看次數: 1057 · 1 分鐘
- Creating Power-Efficient Embedded Solutions with POLARFIRE® SoC FPGA by Microchip Technology  
COMPUTEX · 觀看次數: 4027 · 1 分鐘
- RISC-V Everywhere by RISC-V International  
COMPUTEX · 觀看次數: 3487 · 1 分鐘
- Intel Investments in the RISC-V Ecosystem by Intel  
COMPUTEX · 觀看次數: 4177 · 1 分鐘



- 2023 RISC-V Taipei Day: Unlocking the Possibilities of RISC-V in the AI Era  
COMPUTEX · 觀看次數: 3584 · 直播時間: 3 分鐘
- 2023 RISC-V Taipei Day: RISC-V is Leading Technology in the World  
COMPUTEX · 觀看次數: 3747 · 2 分鐘
- 2023 RISC-V Taipei Day: RISC-V is Inevitable  
COMPUTEX · 觀看次數: 2747 · 1 分鐘
- 2023 RISC-V Taipei Day: Bridging The Divide - Unifying RISC-V Through Binary Translation  
COMPUTEX · 觀看次數: 2747 · 2 分鐘
- 2023 RISC-V Taipei Day: Scalable RISC-V For Digital Transformation  
COMPUTEX · 觀看次數: 1747 · 2 分鐘
- 2023 RISC-V Taipei Day: Extending RISC-V Intelligence From Cloud To Edge  
COMPUTEX · 觀看次數: 1747 · 2 分鐘
- 2023 RISC-V Taipei Day: Trends In AI & Automotive  
COMPUTEX · 觀看次數: 1747 · 2 分鐘

**RISC-V** @risc\_v · Oct 10

Join #RISCVTaipeiDay from anywhere in the world! Here's why you can't miss it:

- Explore the future of open-standard ISA!
- Network with industry leaders!
- Dive into cutting-edge RISC-V innovations!

Register for the Thursday event: [hubs.la/Q024DCXg0](https://hubs.la/Q024DCXg0) #RISCVeverywhere



8 replies, 9 likes, 1.5K views

## 2023 RISC-V Taipei Day

October 12



### IC Tech in the AI Era

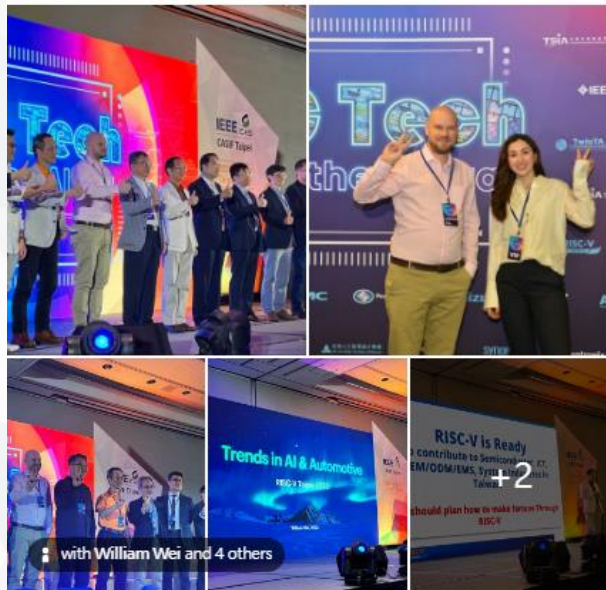
AI development has reached a critical turning point. Large Language Models (LLMs) have attracted great attention recently and require a lot of calculations for AI model training and inference. In the current AI era, IC design will face huge challenges. It will become increasingly difficult to meet performance, power consumption, area and cost goals by continuously shrinking semiconductor processes. Huge computing demands have brought about demands for high performance, high bandwidth and high energy efficiency. Complex IC designs have also increased design costs and the need for highly skilled talents.

AI also brings new opportunities. AI can give new functions to IC products and may also become a key tool for improving efficiency in the IC industry. In addition, open source processor architecture can provide powerful and flexible design components to implement complex AI computing architecture. These have brought revolutionary new opportunities to the IC industry!

**Makeljana S.** • 2nd Growth Strategy & Operations  
2w • [Follow](#)

Wonderful event here at RISC-V Taipei Day, today. Lots of great minds, lots of expertise and lots of passion shared. A variety of successful individuals, from successful companies agreeing on one thing: RISC-V is the future.

RISC-V International #riscv everywhere #riscvtaipeiday VRULL GmbH



# **O4. EVENTS & ACTIVITIES**



# RISC-V Taipei Day



**Closed, Onsite**

**Closed, Online**

**Closed, Hybrid**

**143+ Participants**

**240+ Participants**

**120+ Participants**

**Key Sponsors:**



**Key Sponsors:**



**Key Sponsors:**



# RISC-V Taipei Day

**2023 RISC-V  
Taipei Day**

**10/12** Thu

13:00-17:00 (GMT+8)

台北國際會議中心  
TICC 201會議室  
(台北市信義路五段1號2樓)

會議將使用全英語進行



*Unlocking the Possibilities of RISC-V in the AI Era*  
揭示 AI 時代的新運算技術

**Closed, Hybrid**

**200+ Participants**

**Key Sponsors:**



Powerchip



晶心科技

skymizer

SYNOPSYS®

UMC

MEDIATEK



Syntronix

# 2023 RISC-V Taipei Day

*Unlocking the Possibilities of RISC-V in the AI Era*

**12**

Int'l Speakers

**200+**

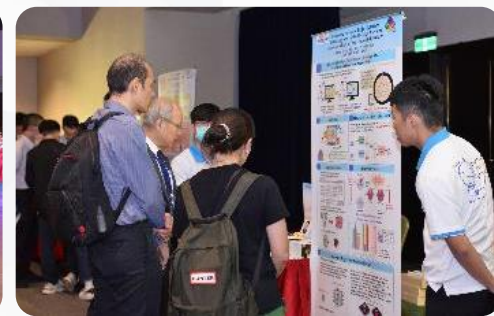
Attendees

**9**

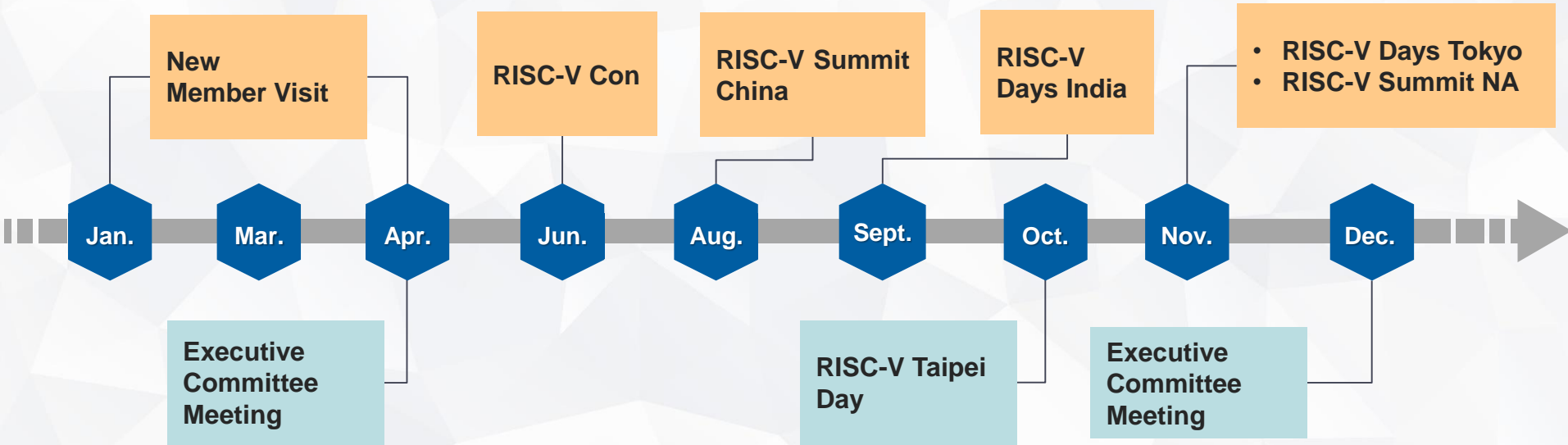
Demo Teams

**7**

Sponsors



# Upcoming Events & Activities



- RISC-V Marketing Committee Monthly Connect: 2<sup>nd</sup> Tuesday of each month
- Online/Offline RISC-V Workshop: (EST. 1~2 times)

# THANK YOU

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Sherry Wang

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